

Freeform Search

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Term:

((combining or combine) adj5 address) same thread

Display: Documents in Display Format: Starting with Number

Generate: ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

Search

Clear

Interrupt

Search History

DATE: Tuesday, November 23, 2004 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>			
<u>L30</u>	((combining or combine) adj5 address) same thread	4	<u>L30</u>
<u>L29</u>	L28 and thread	1	<u>L29</u>
<u>L28</u>	(combine or combining) adj5 mask adj5 address	10	<u>L28</u>
<u>L27</u>	L26 same address	4	<u>L27</u>
<u>L26</u>	static adj2 thread	98	<u>L26</u>
<u>L25</u>	(associate or associating) same mask same thread	14	<u>L25</u>
<u>L24</u>	l23 and address	12	<u>L24</u>
<u>L23</u>	static thread	36	<u>L23</u>
<u>L22</u>	(thread) same address and static	1179	<u>L22</u>
<u>L21</u>	(thread adj4 mask) same address and static	6	<u>L21</u>
<u>L20</u>	(thread adj4 mask) same address andf static	0	<u>L20</u>
<u>L19</u>	(thread adj4 mask) same address	10	<u>L19</u>
<u>L18</u>	thread adj4 mask	137	<u>L18</u>
<u>L17</u>	thread same mask	1925	<u>L17</u>
<u>L16</u>	thread adj2 static	159	<u>L16</u>

<u>L15</u>	thread adj2 static adj2 data	8	<u>L15</u>
<u>L14</u>	L12 and mask and base address	7	<u>L14</u>
<u>L13</u>	L12 and mask	20	<u>L13</u>
<u>L12</u>	L11 and l2	155	<u>L12</u>
<u>L11</u>	l8 and thread	280	<u>L11</u>
<u>L10</u>	l9	0	<u>L10</u>
<i>DB=EPAB; PLUR=YES; OP=ADJ</i>			
<u>L9</u>	thread same l8	0	<u>L9</u>
<i>DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>			
<u>L8</u>	static adj2 variable	1345	<u>L8</u>
<u>L7</u>	address mask same thread	5	<u>L7</u>
<u>L6</u>	l5 and l4	3	<u>L6</u>
<u>L5</u>	(combine or combining) same ((address adj2 mask) or mask) same address	352	<u>L5</u>
<u>L4</u>	L3 and l2	43	<u>L4</u>
<u>L3</u>	address same mask same thread	75	<u>L3</u>
<u>L2</u>	(multi or multiple) adj2 (thread or threaded)	10995	<u>L2</u>
<u>L1</u>	thread	467007	<u>L1</u>

END OF SEARCH HISTORY

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE



Membership Publications/Services Standards Conferences Careers/Jobs

IEEE Xplore®
 RELEASE 1.8

 Welcome
 United States Patent and Trademark Office


» Se.

[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#)
[Quick Links](#)

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced
- ☐ CrossRef

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

IEEE Enterprise

- ☐ Access the IEEE Enterprise File Cabinet

Your search matched **0** of **1097671** documents.A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance Descending** order.**Refine This Search:**

You may refine your search by editing the current search expression or entering a new one in the text box.

static variable and thread and mask

Search☐ Check to search within this result set**Results Key:****JNL** = Journal or Magazine **CNF** = Conference **STD** = Standard**Results:****No documents matched your query.**
Print Format
[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#) | [Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) | [No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2004 IEEE — All rights reserved

Freeform Search

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Term:

((combining or combine) adj5 address) same thread

Display: Documents in Display Format: Starting with Number

Generate: ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

Search

Clear

Interrupt

Search History

DATE: Tuesday, November 23, 2004 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>			
<u>L30</u>	((combining or combine) adj5 address) same thread	4	<u>L30</u>
<u>L29</u>	L28 and thread	1	<u>L29</u>
<u>L28</u>	(combine or combining) adj5 mask adj5 address	10	<u>L28</u>
<u>L27</u>	L26 same address	4	<u>L27</u>
<u>L26</u>	static adj2 thread	98	<u>L26</u>
<u>L25</u>	(associate or associating) same mask same thread	14	<u>L25</u>
<u>L24</u>	l23 and address	12	<u>L24</u>
<u>L23</u>	static thread	36	<u>L23</u>
<u>L22</u>	(thread) same address and static	1179	<u>L22</u>
<u>L21</u>	(thread adj4 mask) same address and static	6	<u>L21</u>
<u>L20</u>	(thread adj4 mask) same address andf static	0	<u>L20</u>
<u>L19</u>	(thread adj4 mask) same address	10	<u>L19</u>
<u>L18</u>	thread adj4 mask	137	<u>L18</u>
<u>L17</u>	thread same mask	1925	<u>L17</u>
<u>L16</u>	thread adj2 static	159	<u>L16</u>

<u>L15</u>	thread adj2 static adj2 data	8	<u>L15</u>
<u>L14</u>	L12 and mask and base address	7	<u>L14</u>
<u>L13</u>	L12 and mask	20	<u>L13</u>
<u>L12</u>	L11 and l2	155	<u>L12</u>
<u>L11</u>	l8 and thread	280	<u>L11</u>
<u>L10</u>	l9	0	<u>L10</u>
<i>DB=EPAB; PLUR=YES; OP=ADJ</i>			
<u>L9</u>	thread same l8	0	<u>L9</u>
<i>DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>			
<u>L8</u>	static adj2 variable	1345	<u>L8</u>
<u>L7</u>	address mask same thread	5	<u>L7</u>
<u>L6</u>	l5 and l4	3	<u>L6</u>
<u>L5</u>	(combine or combining) same ((address adj2 mask) or mask) same address	352	<u>L5</u>
<u>L4</u>	L3 and l2	43	<u>L4</u>
<u>L3</u>	address same mask same thread	75	<u>L3</u>
<u>L2</u>	(multi or multiple) adj2 (thread or threaded)	10995	<u>L2</u>
<u>L1</u>	thread	467007	<u>L1</u>

END OF SEARCH HISTORY